

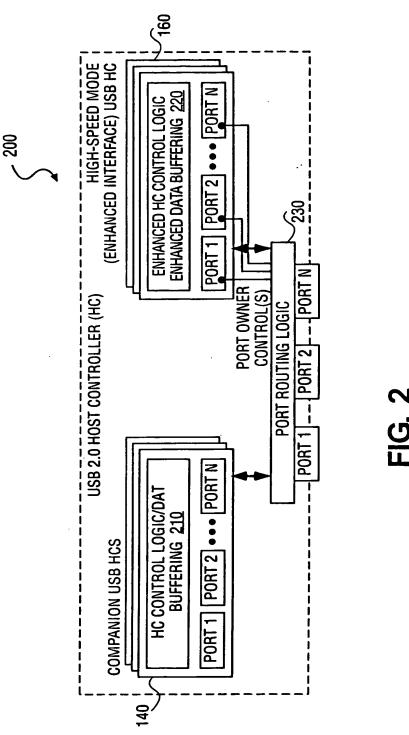
FIG. 1



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HUBS

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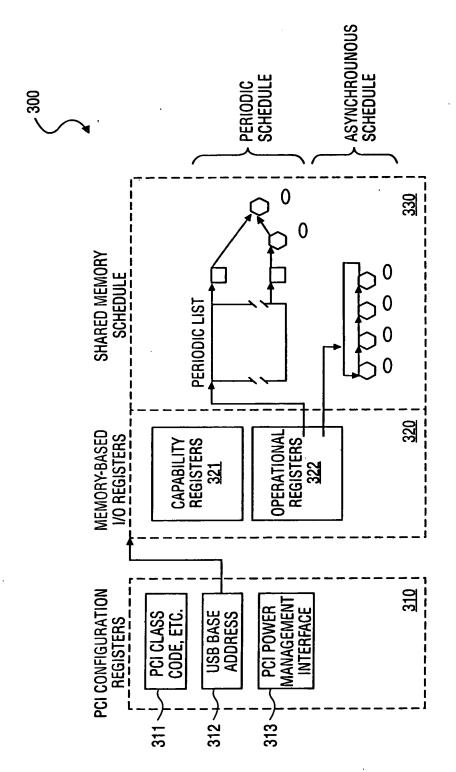


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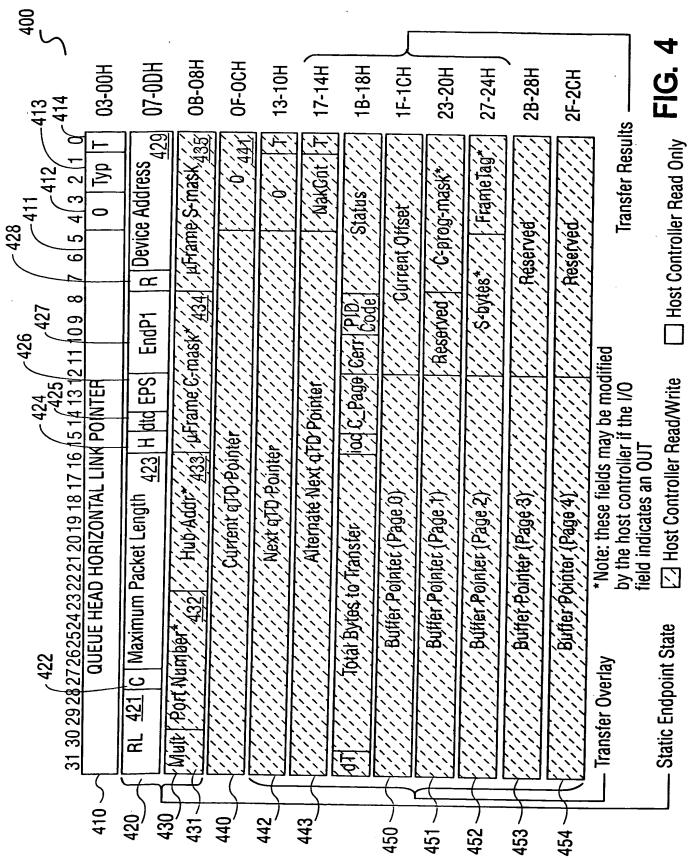


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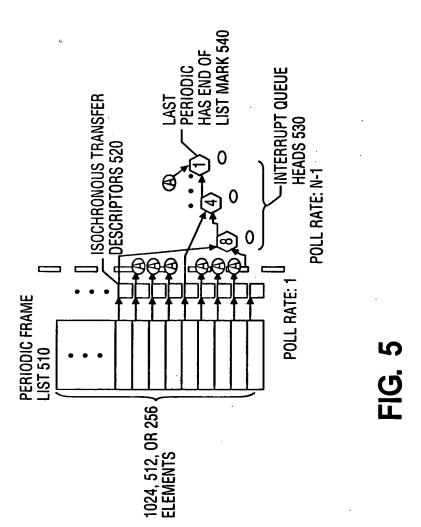
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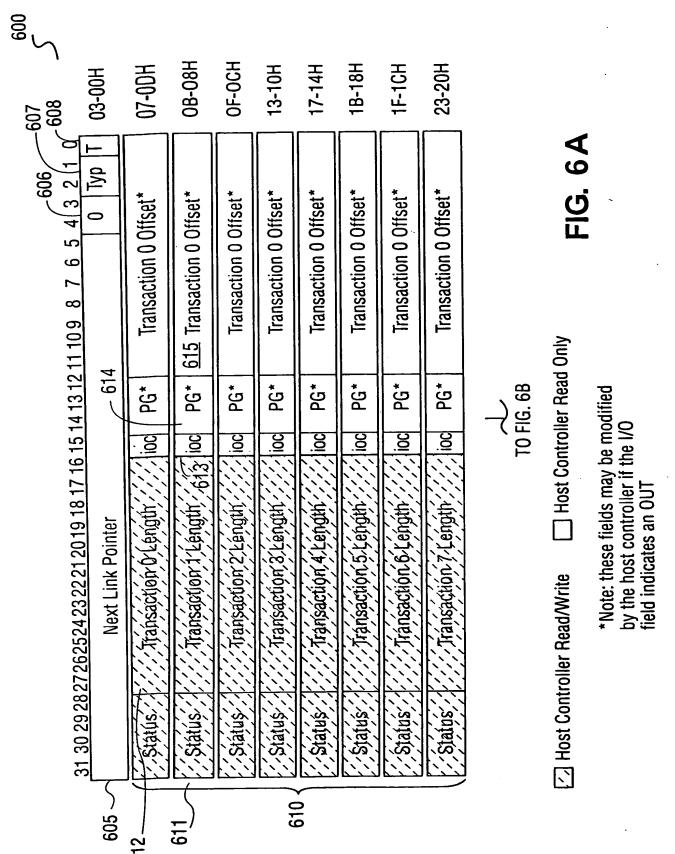
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4 2 9 ∞ 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9

FROM FIG. 6A

	Buffer Pointer (Page 0)	EndPt R	œ	Device Address	ress	27-24H
	Buffer Pointer (Page 1)	0/1	Maxi	Maximum Packet Size	Ze	2B-28H
	Buffer Pointer (Page 2)			Reserved	Mult	2F-2CH
e20 \	Buffer Pointer (Page 3)	,		Reserved		33-30Н
	Buffer Pointer (Page 4)			Reserved		37-34H
	Buffer Pointer (Page 5)			Reserved		3B-38H
	Buffer Pointer (Page 6)			Reserved		3F-3CH

FIG. 6B

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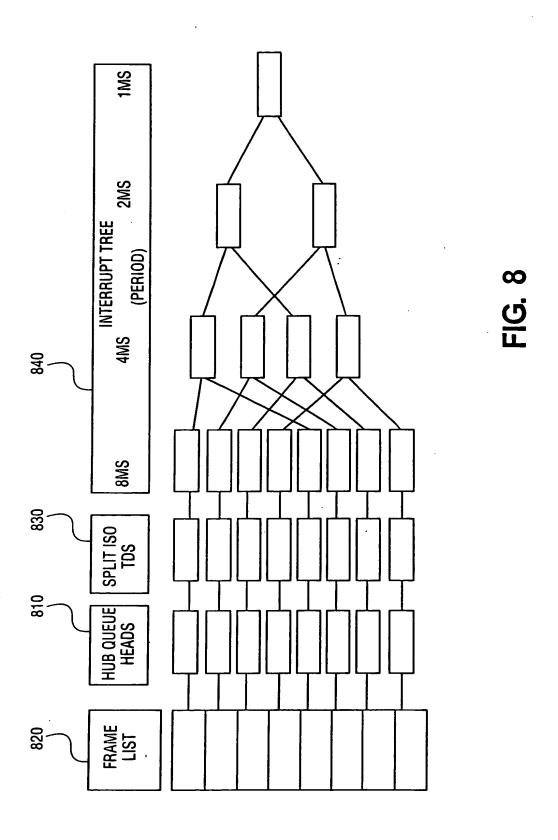
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17-14H 1B-18H 02-00H 07-04H 0B-08H 13-10H **OF-0CH Device Address** 17-eount **Transfer Results** μFrame S-mask Τyp 0 က <u>à</u> S 9 / 2 Œ Reserved 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 EndPt μFrame C-mask ☐ Host Controller Read Only α **Next Link Pointer** Back Pointer Buffer Pointer (Page 0) Buffer Pointer (Page 1) **Hub Addr** [2] Host Controller Read/Write Reserved α Static Endpoint State Port Number 720

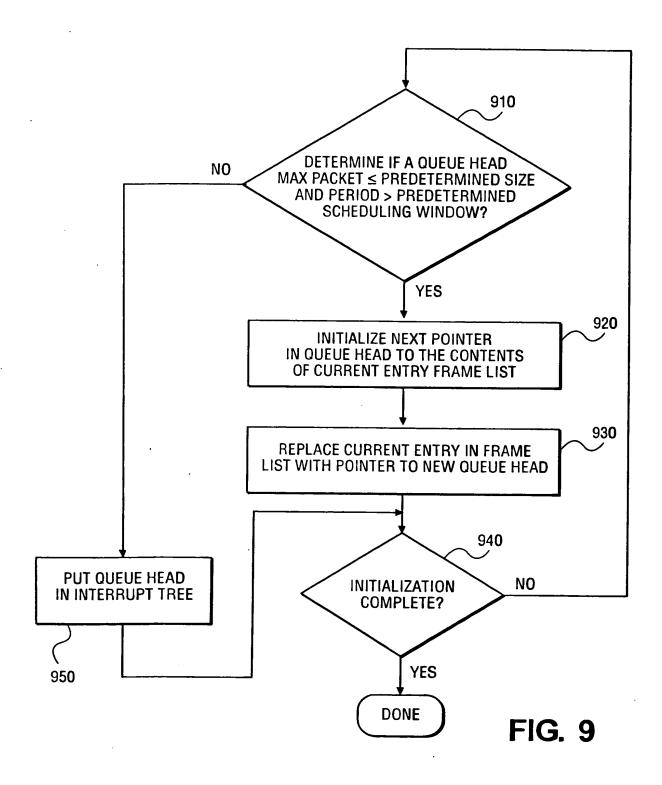
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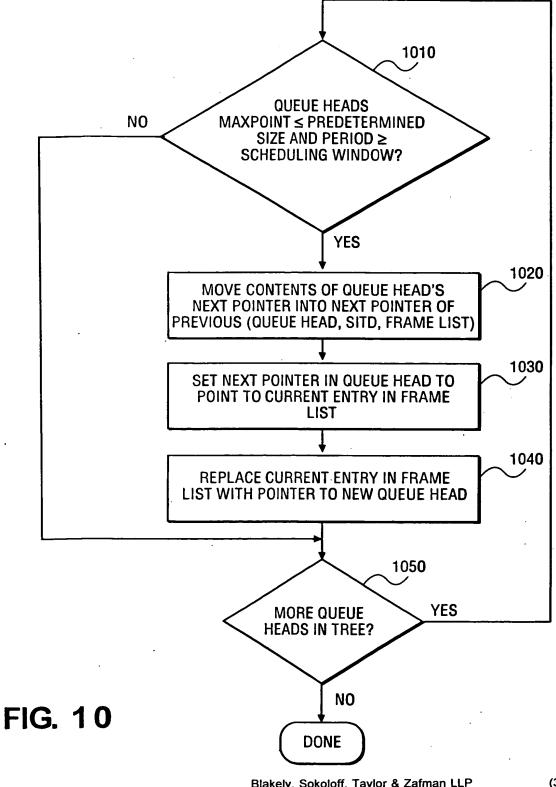
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